



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor Application of : Prashanth Bhat et al.

Examiner: Lewis A. Bullock, Jr.

Application No. : 09/865,988

Group Art Unit: 2195

Filing Date : May 25, 2001

Title : LOAD BALANCING SYSTEM AND METHOD IN A  
MULTIPROCESSOR SYSTEM

Mail Stop: Amendment  
Commissioner for Patents  
PO Box 1450  
Alexandria VA 22313-1450

**AMENDMENT AND RESPONSE TO OFFICE ACTION**

Sir:

In response to the Office Action dated May 3, 2006, please reconsider the above-identified application in light of the following:

**Amendments to the Claims** begin on page 2 of this paper; and

**Remarks** begin on page 15 of this paper.